

Class : XIIth Date : Subject : PHYSICS DPP No. : 10

Topic :-.semiconductor electronics: materials, devies and simple circuits

1. The current gain of a transistor in a common emitter configuration is 40. If the en							
	is 8.2 mA, then base						
	a) 0.02 mA	b) 0.2 mA	c) 2.0 mA	d) 0.4 mA			
2.	In order to prepare a <i>p</i> -type semiconductor, pure silicon can be doped with						
	a) Phosphorus	b) Aluminium	c) Antimony	d)Germanium			
3.	The current in a trio	—1.2 <i>V</i> is 7.5 <i>mA</i> . If grid					
	potential is changed to $-2.2 V$, the current becomes 5.5 <i>mA</i> . the value of trans conductance (
	g_m) will be						
	a) 2 mili mho	b) <mark>3 mili</mark> mho	c) 4 mili mho	d) 0.2 mili mho			
4.	If A and B are two in	puts in AND gate, then	AND gate has an output	of 1 when the values of A			
	and <i>B</i> are						
	a) <i>A</i> = 0, <i>B</i> = 0	b) <i>A</i> = 1, <i>B</i> = 1	c) <i>A</i> = 1, <i>B</i> = 0	d) $A = 0, B = 1$			
5.	A gate in which all the inpu <mark>ts mu</mark> st be low to get a high output is called						
	a) A NAND gate	b) <mark>An inv</mark> erter	c) A NOR gate	d)An AND gate			
6.	A NPN transistor conducts when						
	a) Both collector and emitter are positive with respect to the base						
b) Collector is positive and emitter is negative with respect to the base							
	c) Collector is positiv						
	d) Both collector and emitter are negative with respect to the base						
7.	An alternating voltag	e as shown in the figure. The					
	maximum potential difference across the condenser will be						
	P						
	141.4						
	$\int ac (rms) K + F + F$						
	a) 100 V	b) 200 V	c) $100\sqrt{2} V$	d) $200\sqrt{2}V$			

8. A zener diode, having breakdown voltage equal to 15 *V*, is used in a voltage regulator circuit shown in figure. The current through the diode is



	- 0							
	a) 20 <i>mA</i>	b) 5 <i>mA</i>	C	c) 10 mA	d) 15 <i>mA</i>			
9.	For the given circuit of	PN-junctio	n diode, which	of the following	g statement is correct			
	a) In forward biasing the voltage across <i>R</i> is <i>V</i> b) In forward biasing the voltage across <i>R</i> is 2 <i>V</i>							
	c) In reverse biasing the voltage across <i>R</i> is <i>V</i>							
	d) In reverse biasing th	e vo <mark>ltage a</mark>	cross R is 2V					
10.	Current gain in commo	n em <mark>itter</mark> c	onfig <mark>uratio</mark> n is	more than 1 be	ecomes			
	a) $I_c < I_b$	b)I _c < I _e	C	$I_c > I_e$	d) $I_e > I_b$			
11.	The reason of current f	low <mark>in <i>P-N</i></mark>	junction in for	ward bias is				
	a) Drifting of charge car	rriers	k	o) Minority char	ge carriers			
	c) Diffusion of charge c	arriers	C	l) All of these				
12.	A triode whose mutual conductance is $2.5 m A/volt$ and anode resistance is $20 kilo ohm$, is							
	used as an amplifier whose amplification is 10. The resistance connected in plate circuit will be							
4.0	a) 1 $k\Omega$	b)5 <i>k</i> Ω	(c) 10 $k\Omega$	d) 20 $k\Omega$			
13.	In the forward bias arrangement of a <i>PN</i> -junction diode							
	a) The <i>N</i> -end is connected to the positive terminal of the battery							
	b) The P-end is connected to the positive terminal of the battery							
	c) The direction of current is from <i>N</i> -end to <i>P</i> -end in the diode							
14	a) The <i>P</i> -end is connected to the negative terminal of battery							
14.	If the forward voltage in a semiconductor diode is changed from 0.5 V to 0.7 V, then the							
	iorward current change	es by 1.0 m	A. The forward	resistance of a	a) 240 O			
15	a) 100 12	0)12032	Ĺ	IC	u)240 M			
13.	For a common base cor	en maximum current gain in						
	common emitter configuration will be							
	a) 12	b)24	C	:) 6	d)5			

